isc Three Terminal Positive Voltage Regulator 7805

FEATURES

- Output current in excess of 1.5A
- Output voltage of 5V
- Internal thermal overload protection
- Output transition Safe-Area compensation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

1 2 3

PIN 1, Input
2, Ground
3, Output

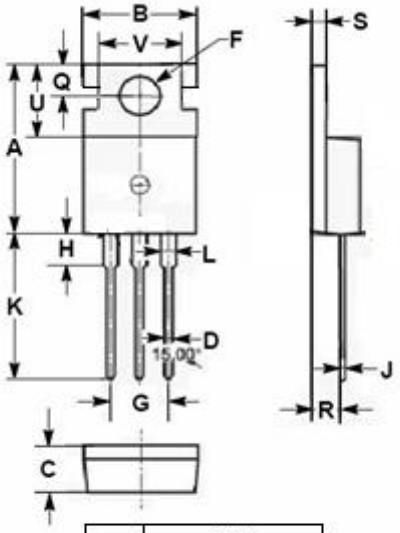
TO-220C package

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	RATING	UNIT
V_i	DC input voltage	35	V
I_o	Output current	internally limited	
P_{tot}	Power dissipation	internally limited	
T_{OP}	Operating junction temperature	0~150	℃
T_{stg}	Storage temperature	-55~150	℃

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j\text{-}c}$	Thermal Resistance, Junction to Case	3	℃/W
$R_{th\ j\text{-}a}$	Thermal Resistance,Junction to Ambient	50	℃/W



DIM	mm MIN	MAX
A	15.50	15.90
B	9.80	10.20
C	4.20	4.50
D	0.70	0.90
F	3.40	3.70
G	4.98	5.18
H	2.68	2.90
J	0.44	0.60
K	12.80	13.40
L	1.20	1.45
Q	2.70	2.90
R	2.30	2.70
S	1.29	1.35
U	6.45	6.65
V	8.66	8.86

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• ELECTRICAL CHARACTERISTICS

T_j=25℃ (V_i= 10V, I_O=0.5A, C_i= 0.33μF, C_o= 0.1μF unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_O	Output Voltage	V_{in}=20V; I_O=500mA	4.8	5.2	V
$\triangle V_V$	Line Regulation	7.5V≤V_{in}≤20V; I_O=0.5A		50	mV
$\triangle V_i$	Load Regulation	5.0mA≤I_O≤1.5A;V_{in}=10V		100	mV
I_q	Quiescent Current	V_{in}=10V; I_O=1.5A		6.0	mA
$\triangle_{q1}$	Quiescent Current Change	5.0mA≤I_O≤1.0A;V_{in}=10V		0.5	mA
$\triangle_{q2}$	Quiescent Current Change	7V≤V_{in}≤25V; I_O=500mA		1.3	mA