

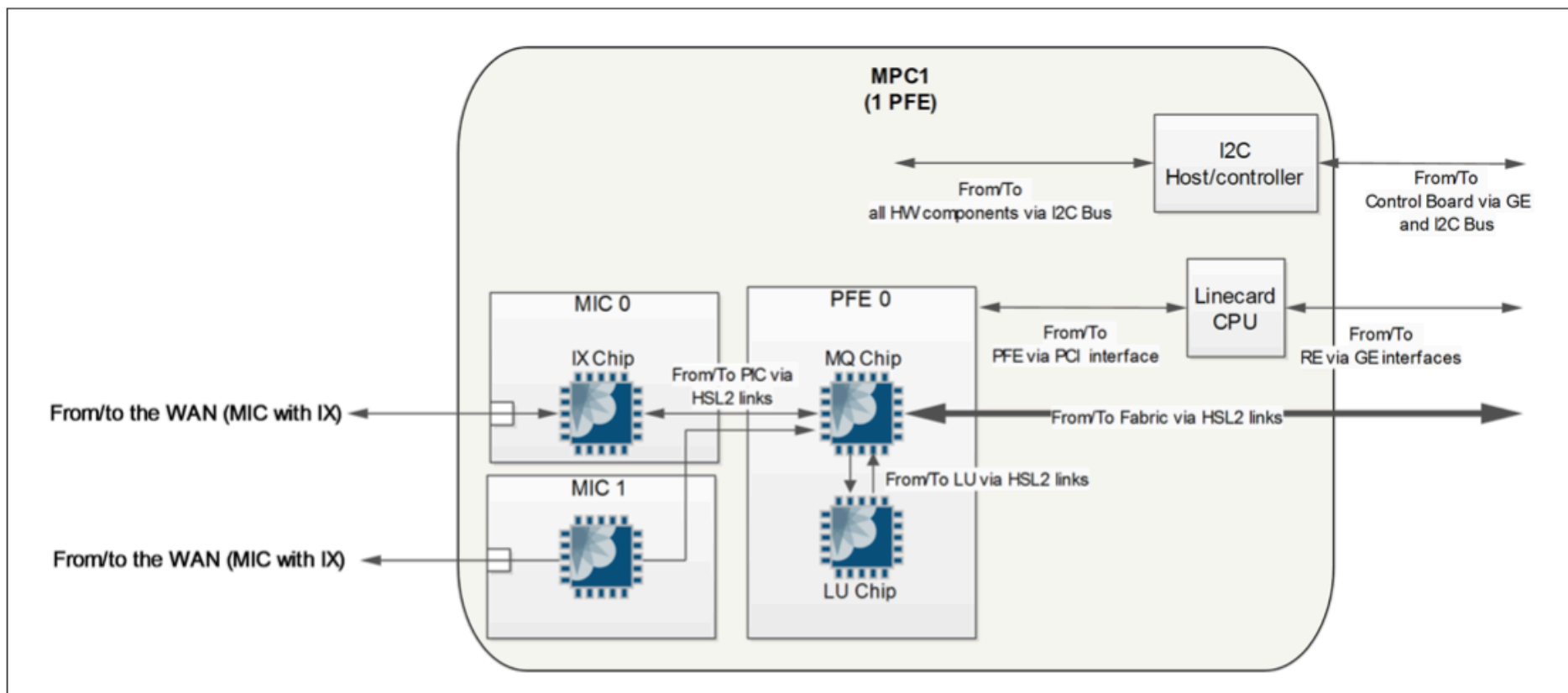


Figure 1-32. MPC1 architecture

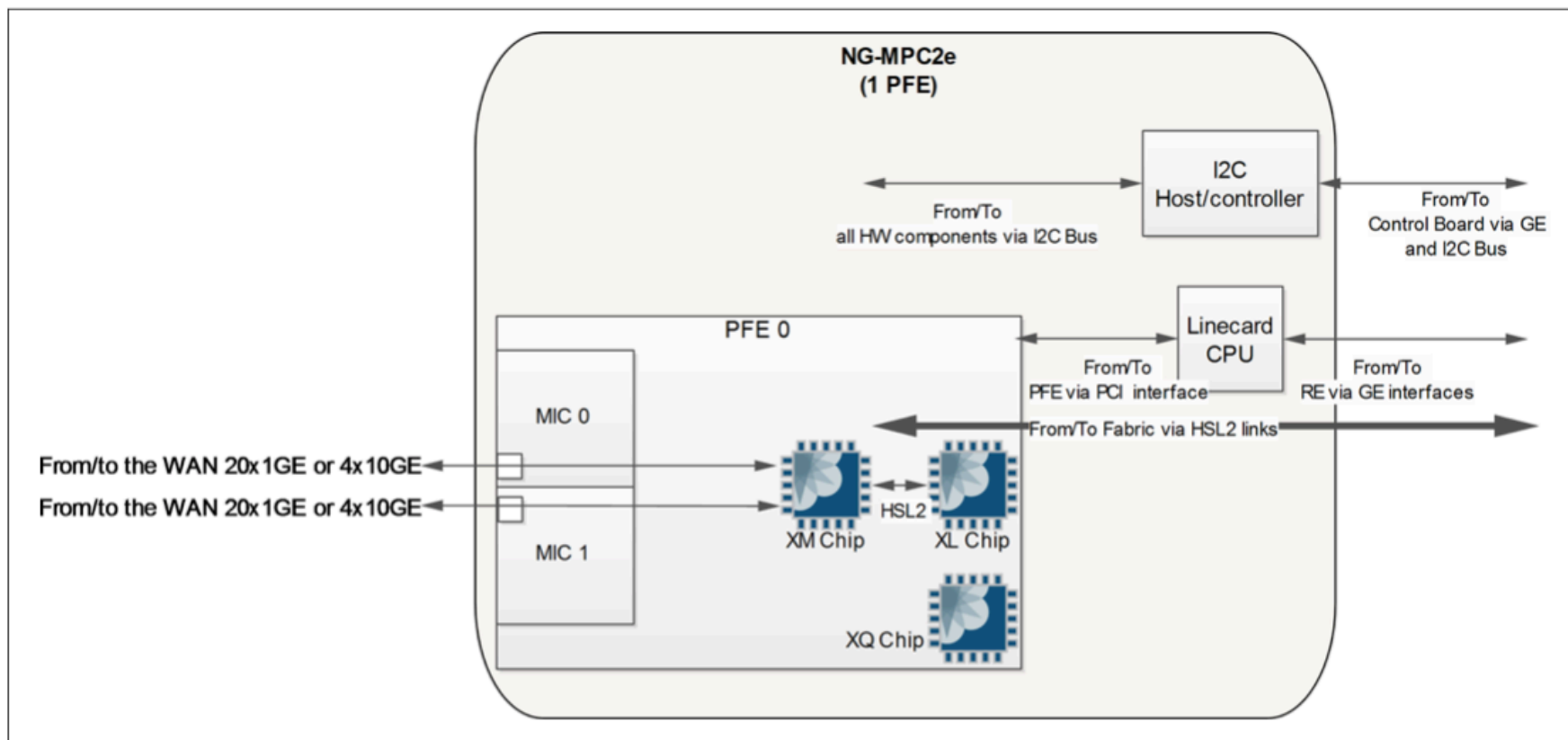


Figure 1-41. NG-MPC2e architecture

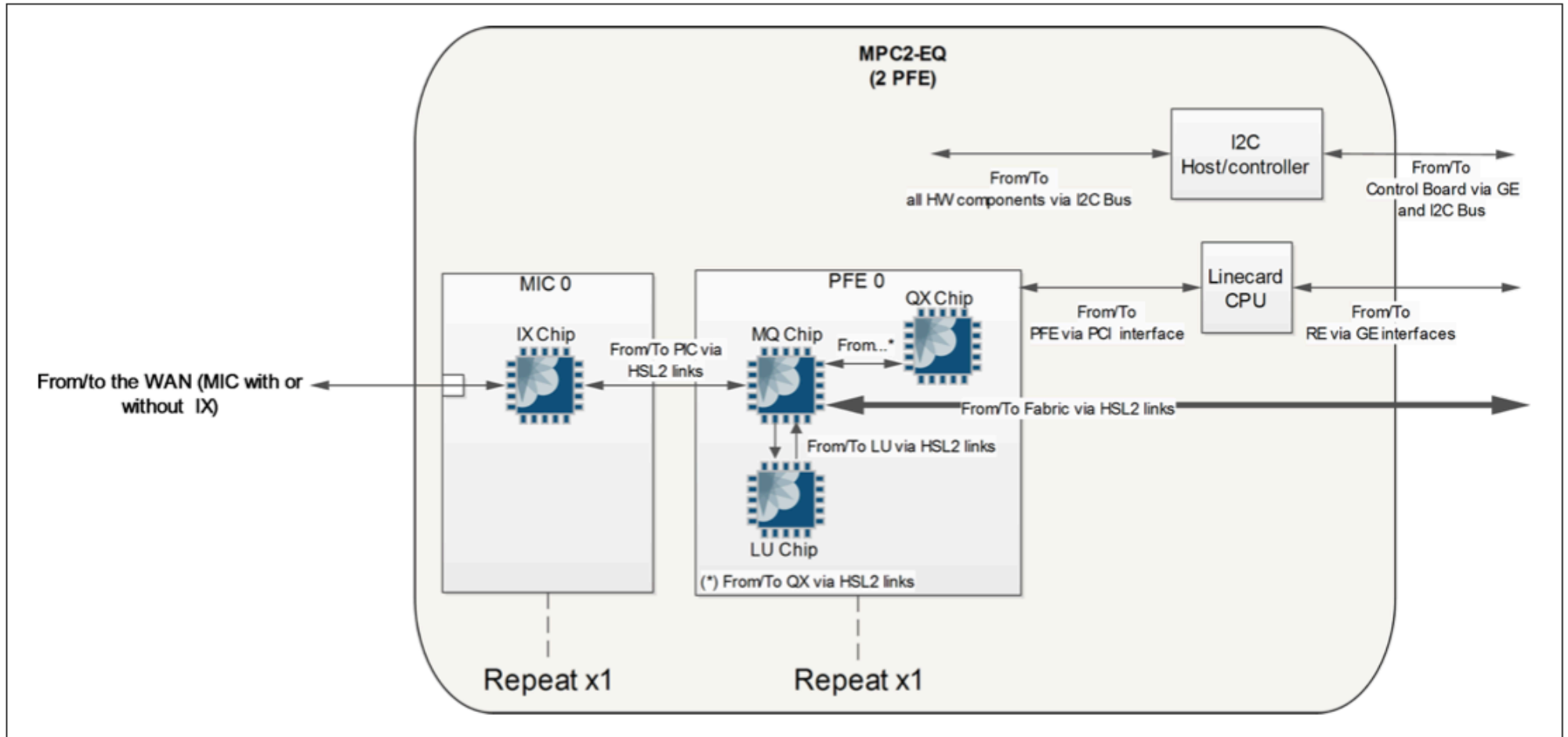


Figure 1-33. MPC2 architecture

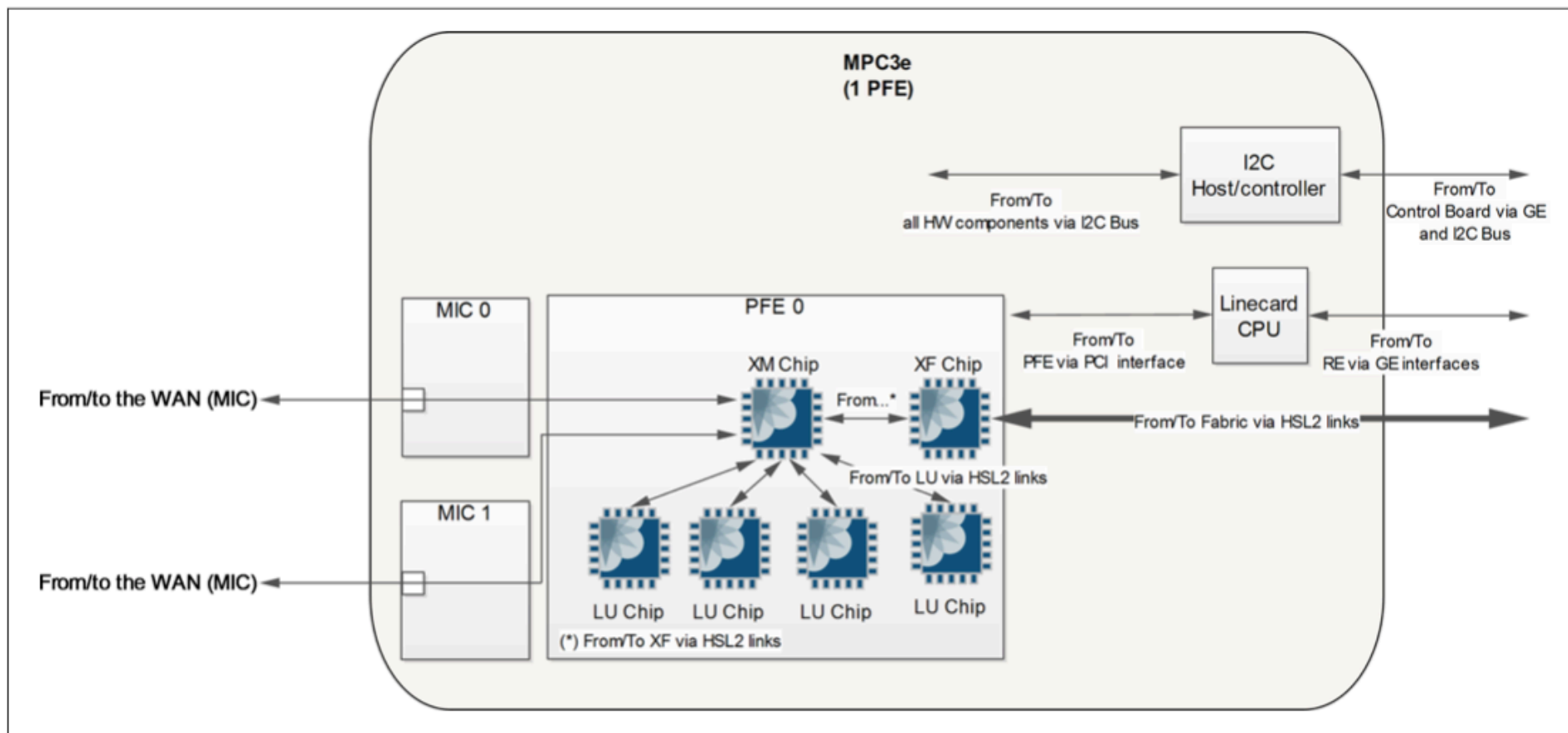


Figure 1-35. MPC3E architecture

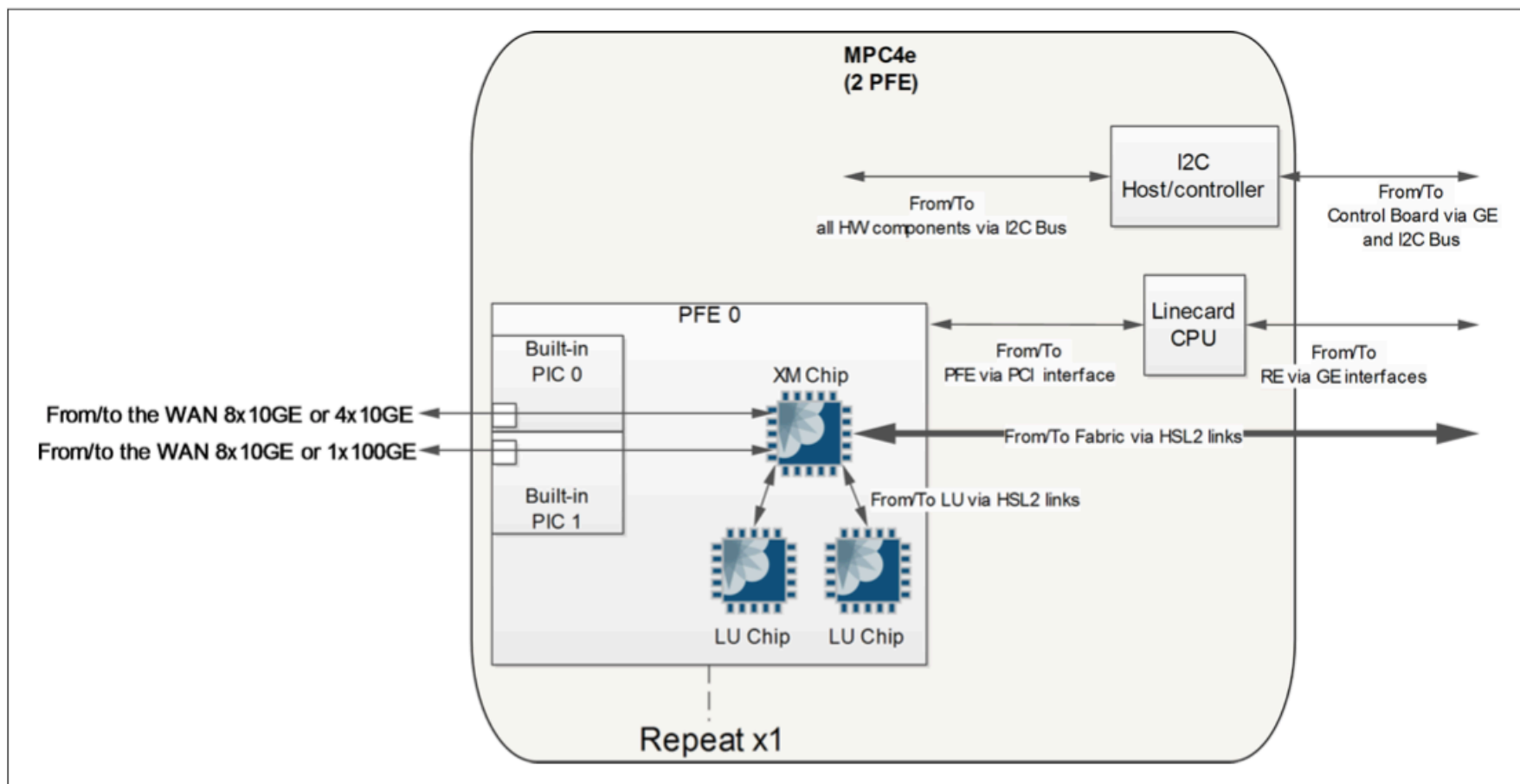


Figure 1-38. MPC4e architecture

shown in Figure 1-39. The second model is fully line rate.

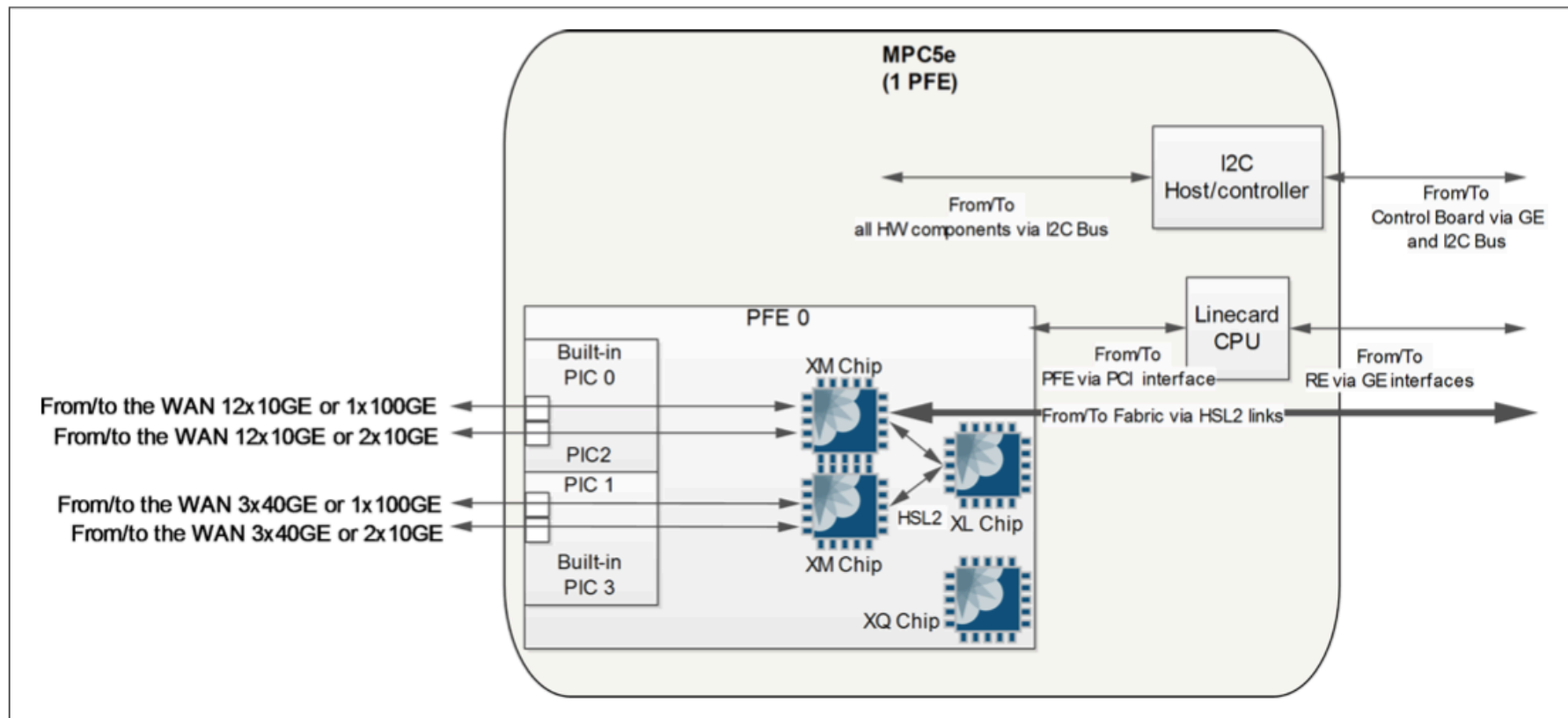


Figure 1-39. MPC5e architecture

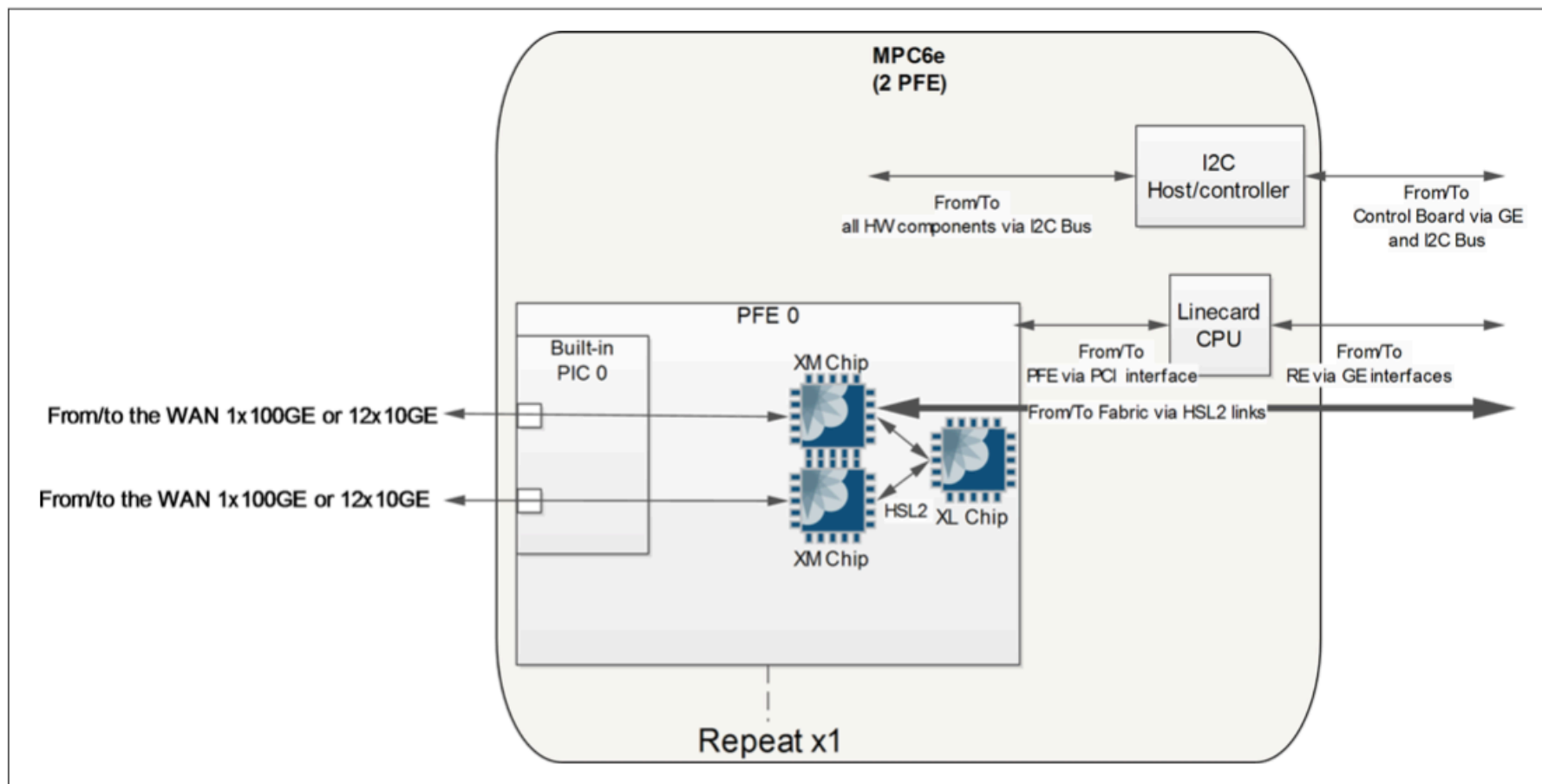


Figure 1-40. MPC6e architecture

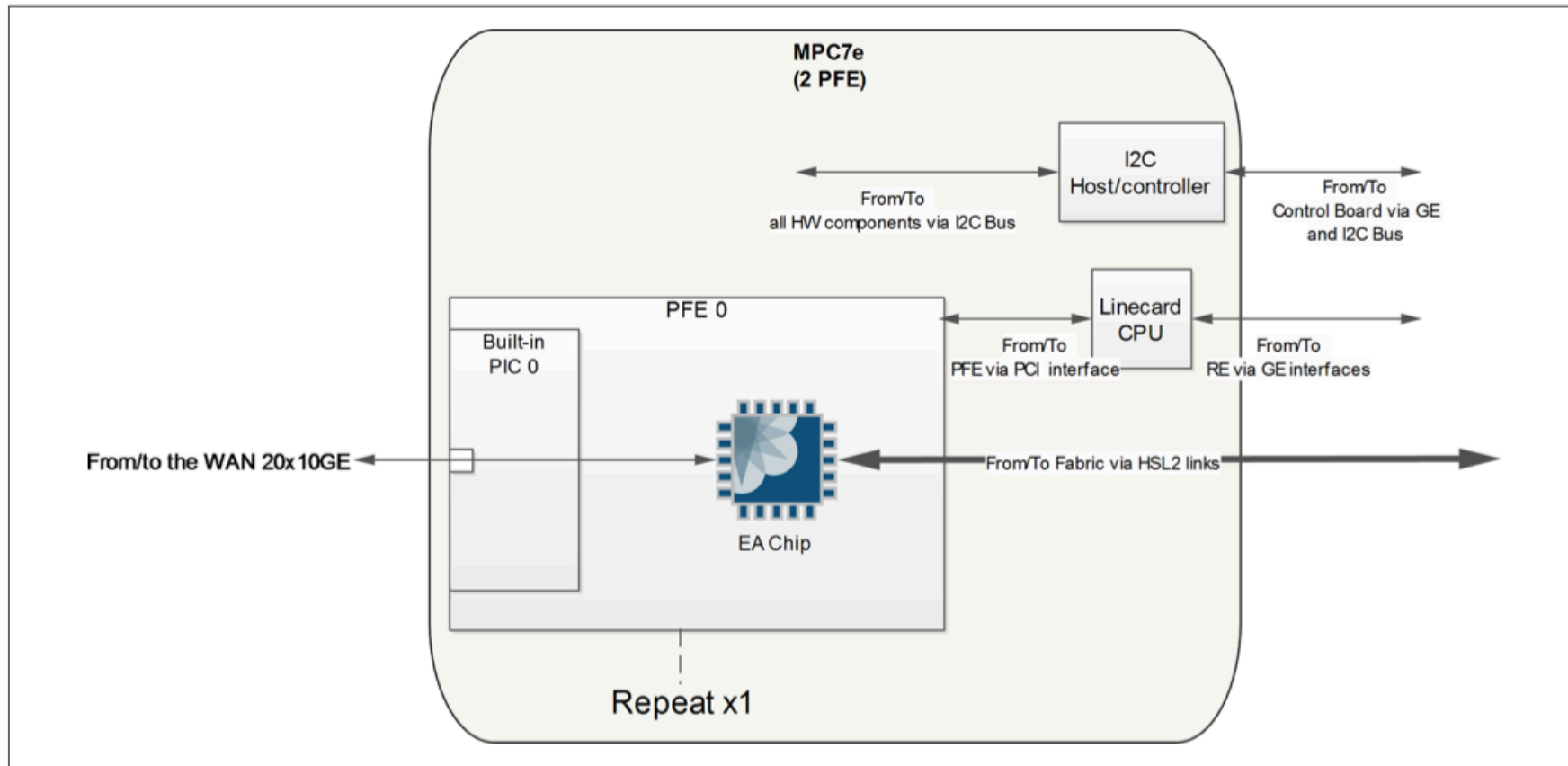


Figure 1-43. MPC7e 10GE architecture



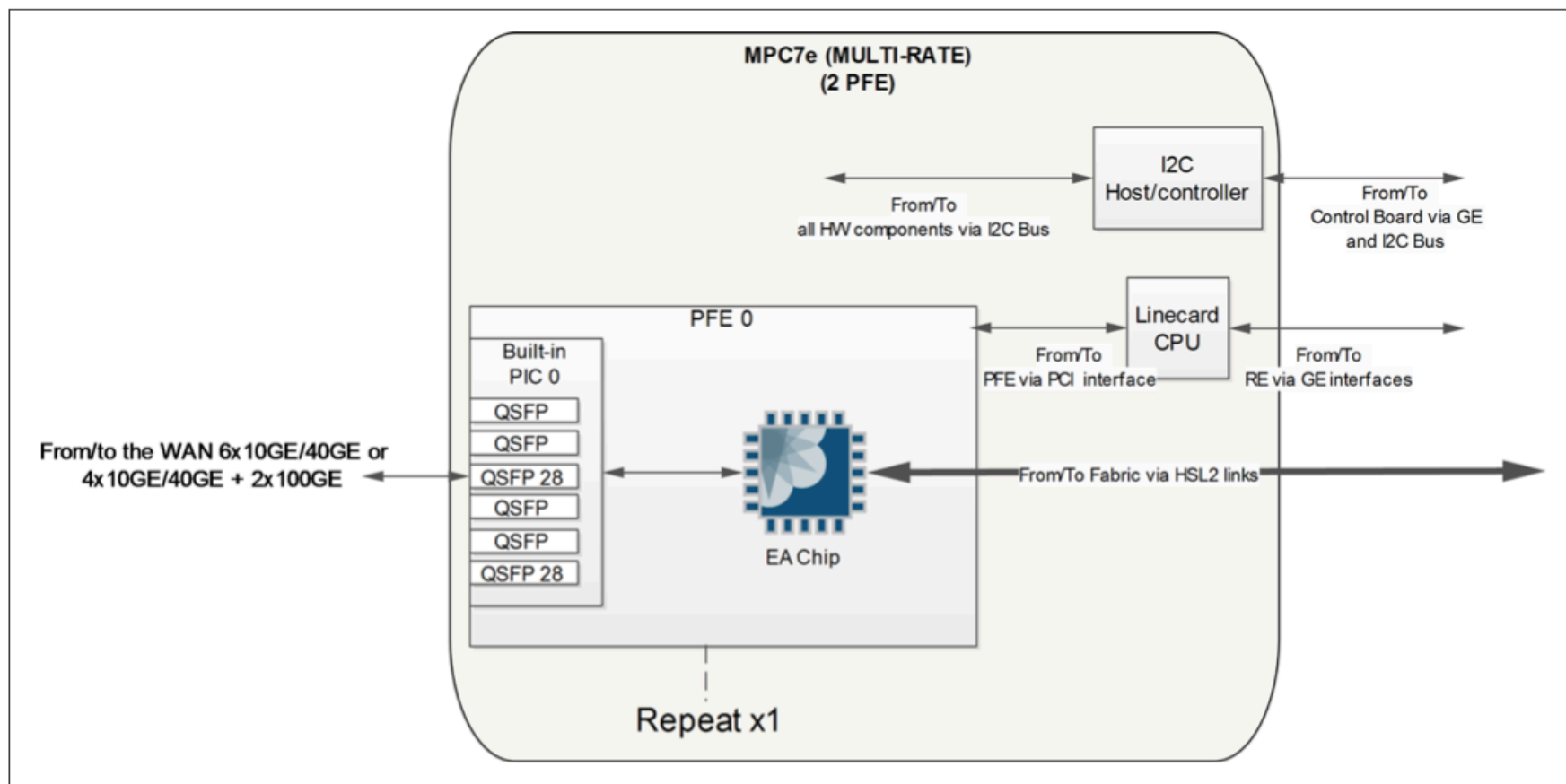


Figure 1-42. MPC7e multi-rate architecture

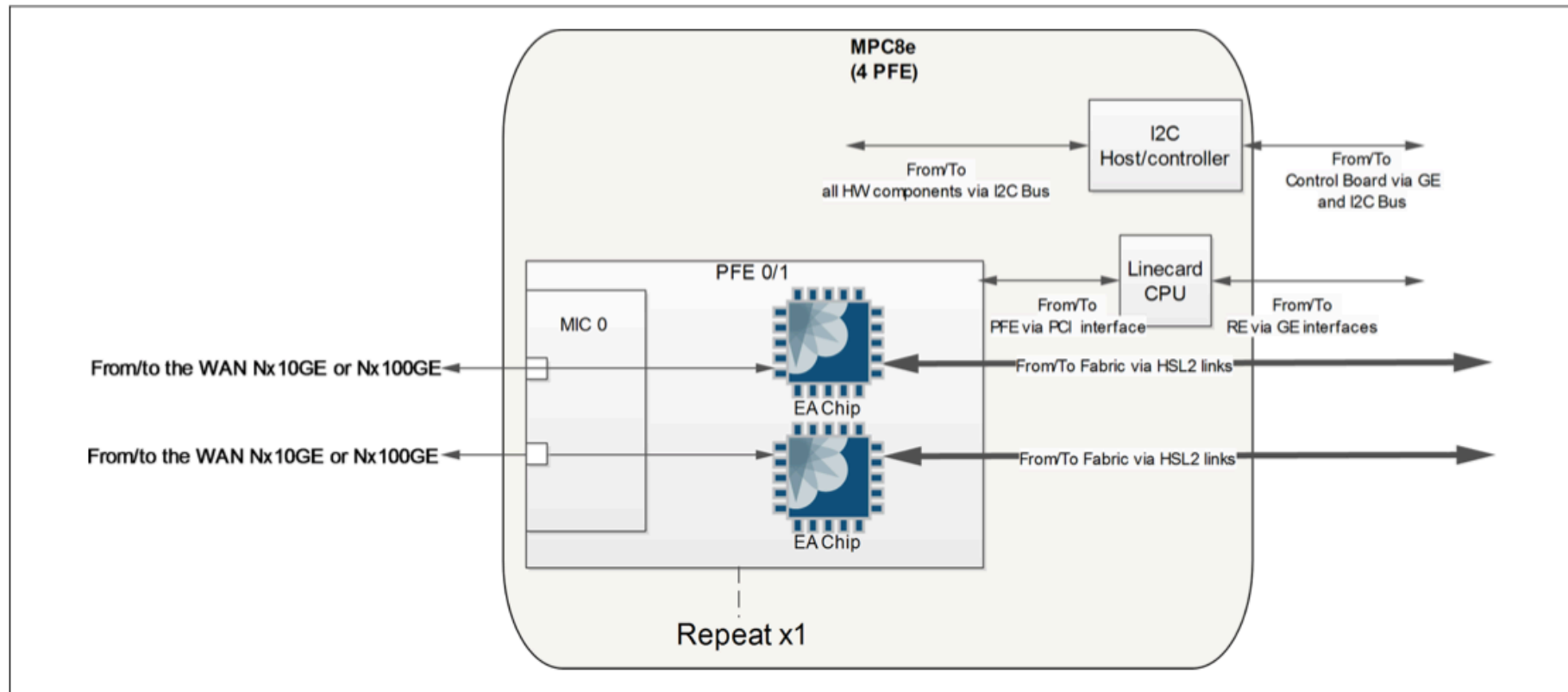


Figure 1-44. MPC8e architecture

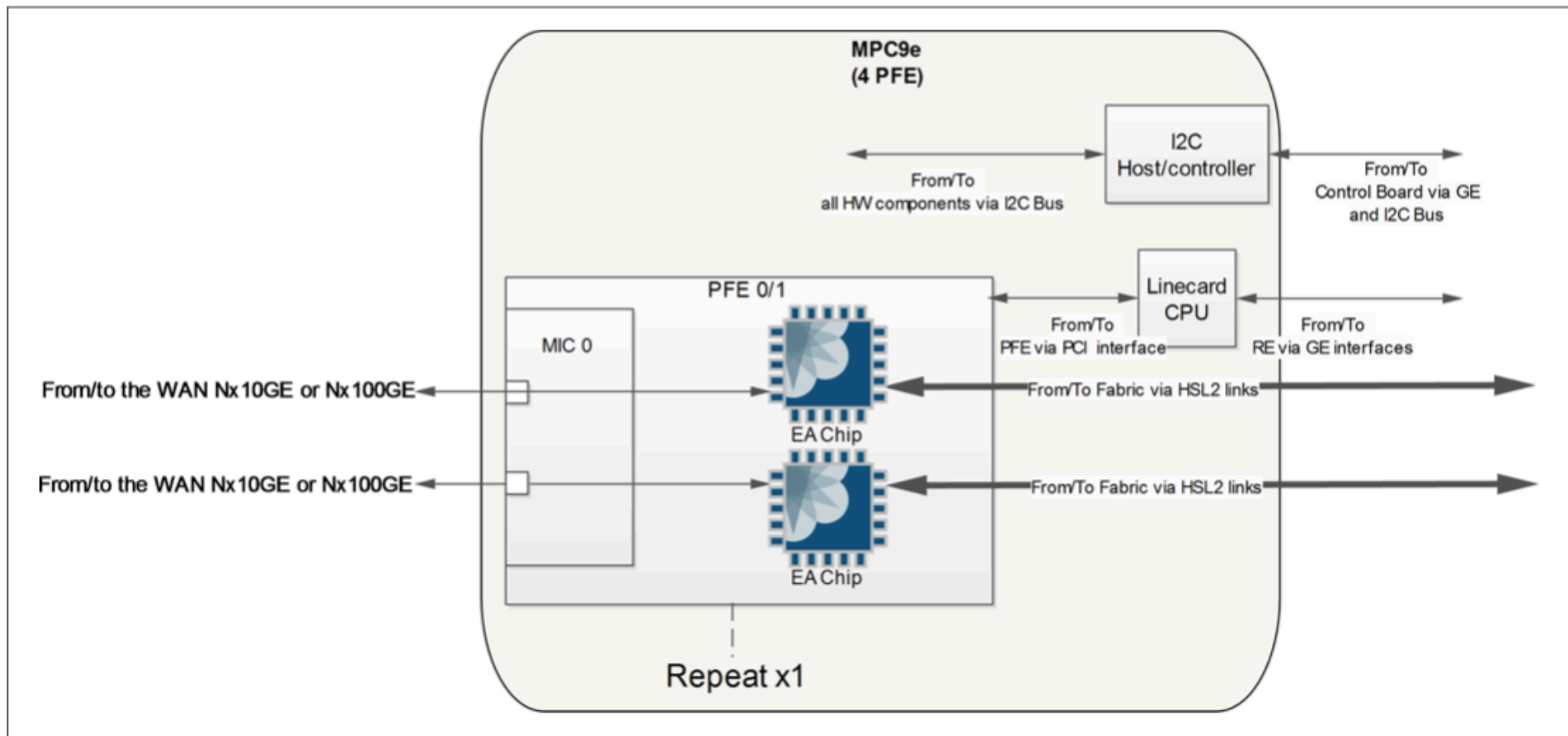


Figure 1-45. MPC9e architecture

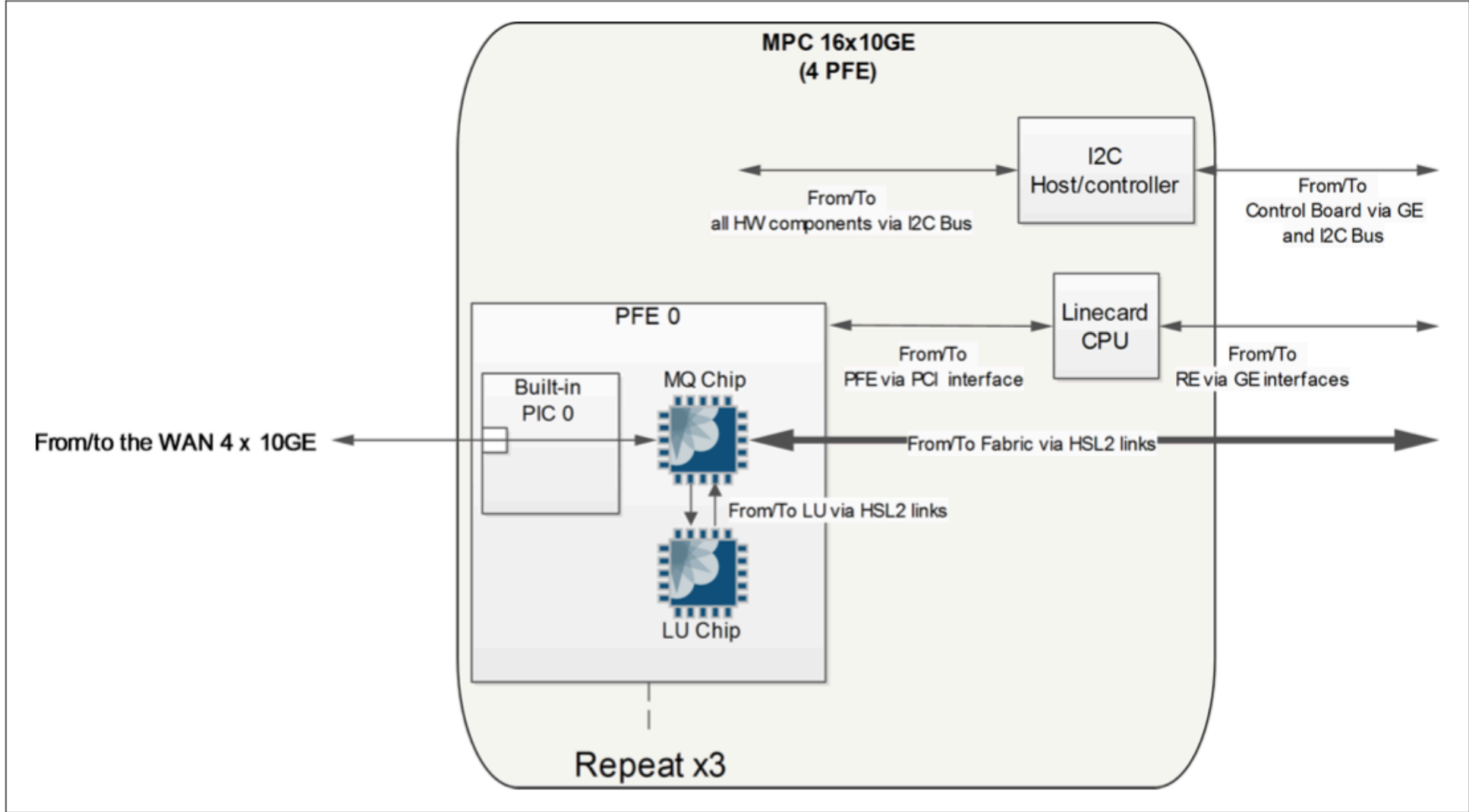


Figure 1-10: MPC 16x10GE (4 PFE) Architecture